

	L #	Hits	Search Text	DBs
1	L1	39	multiplex\$3 near50 ((bypass\$3 (by adj1 pass\$3)) near20 register) near99 (alu ((arithmetic logic) adj1 unit))	US-PGPUB; USPAT
2	L2	78	vliw and multiplex\$3 near30 (register near20 (alu ((arithmetic logic) adj1 unit)))	US-PGPUB; USPAT
3	L3	63	vliw and multiplex\$3 near30 (register near30 (bypass\$3 (by adj1 pass\$3)))	US-PGPUB; USPAT
4	L5	1	multiplex\$3 near50 ((bypass\$3 (by adj1 pass\$3)) near20 register) near99 (alu ((arithmetic logic) adj1 unit))	USOCR; EPO; JPO; DERWENT; IBM_TDB
5	L6	1	vliw and multiplex\$3 near30 (register near20 (alu ((arithmetic logic) adj1 unit)))	USOCR; EPO; JPO; DERWENT; IBM_TDB
6	L7	1	multiplex\$3 near30 (register near30 (bypass\$3 (by adj1 pass\$3)) near30 ((function\$3 process\$3 comput\$7 operation\$3)adj1 (unit module element)))	USOCR; EPO; JPO; DERWENT; IBM_TDB
7	L9	66	multiplex\$3 near30 (register near30 (bypass\$3 (by adj1 pass\$3)) near30 ((function\$3 process\$3 comput\$7 operation\$3)adj1 (unit module element)))	US-PGPUB; USPAT
8	L14	33	L9 not (1 2 3)	US-PGPUB; USPAT
9	L15	6	vliw and cycle adj1 (bit flag) near30 (instruction operation)	US-PGPUB; USPAT